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Picazo, Jr. et al.

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[54] **NETWORK CONNECTOR OPERABLE IN BRIDGE MODE AND BYPASS MODE**

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[*] Notice: The term of this patent shall not extend beyond the expiration date of Pat. No. 5,432,907.

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Related U.S. Application Data

[60] Division of Ser. No. 498,116, Jul. 5, 1995, Pat. No. 5,742,760, which is a continuation-in-part of Ser. No. 881,931, May 12, 1992, Pat. No. 5,432,907.

[51] **Int. Cl.**⁶ **H04J 3/02**

[52] **U.S. Cl.** **395/200.79; 370/401; 370/351**

[58] **Field of Search** **370/401-404, 370/351; 395/200, 200.02, 200.06, 200.1, 200.2, 200.8, 200.03, 200.72, 200.73, 200.79**

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[57] **ABSTRACT**

A hub circuit with an integrated bridge circuit carried out in software including a switch for bypassing the bridge process such that the two bridged networks effectively become one network. An in-band management process in software is disclosed which receives and executes network management commands received as data packets from the LANs coupled to the integrated hub/bridge. Also, hardware and software to implement an isolate mode where data packets which would ordinarily be transferred by the bridge process are not transferred except in-band management packets are transferred to the in-band management process regardless of which network from which they arrived. Also disclosed, a packet switching machine having shared high-speed memory with multiple ports, one port coupled to a plurality of LAN controller chips coupled to individual LAN segments and an Ethernet microprocessor that sets up and manages a receive buffer for storing received packets and transferring pointers thereto to a main processor. The main processor is coupled to another port of the memory and analyzes received packets for bridging to other LAN segments or forwarding to an SNMP agent. The main microprocessor and the Ethernet processor coordinate to manage the utilization of storage locations in the shared memory. Another port is coupled to an uplink interface to higher speed backbone media such as FDDI, ATM etc. Speeds up to media rate are achieved by only moving pointers to packets around in memory as opposed to the data of the packets itself. A double password security feature is also implemented in some embodiments to prevent accidental or intentional tampering with system configuration settings.

4 Claims, 13 Drawing Sheets

